

## David L. Huffman **Engineering Note**

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**Project:** BLS Modifications for 2003

**Doc. No:** h030331

Subject: BLS Regulator Board Modifications

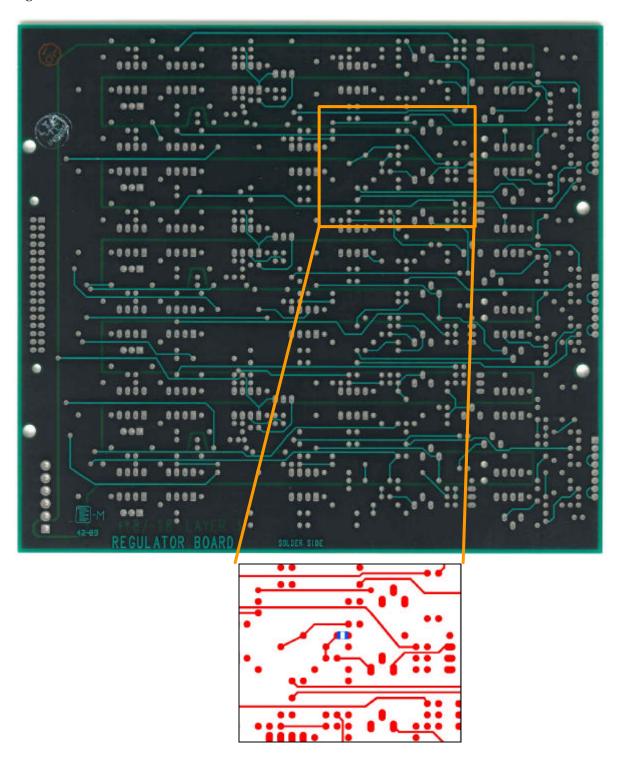
## 1. Overview

1.1. There are two modifications to be done on the BLS power supply that will prevent or correct DC output problems. The first modification suppresses any chance of oscillation that has been seen on one of the current sources. This change requires a capacitor to be placed as indicated in the figures below. The second modification increases the output voltage of the logic supply to accomidate the increase in voltage drop on the wiring harness due to an increased current draw. This change involves adding a resistor (not currently installed) into the R302 location.

## 2. Capacitor Modification

2.1. On the bottom of the Regulator board add a 0.1 uF chip capacitor across the pads at the location shown in Figure 1.

Figure 1



2.2. Add a 3.6K resistor to the top of the Regulator board as shown in the figure below. This goes in the R302 position seen in blue.

